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Oct. 27th PV Modules: Market & Technology Trends (10:00-12:00)

GLOBAL OPTIMIZATION OF INTEGRATED PHOTOVOLTAIC SYSTEM FOR LOW ELECTRICITY COST





PV Modules: Market & Technology Trends



Session Contents

- 1. From solar cells to modules (Olatz Arriaga)
- 2. Materials and processes used in solar module manufacturing (Luca Gnocchi)
- 3. Targeting PV module service lifetimes of 35+ years (Dr. Alessandro Virtuani)



October 26-29th 2020



PV Modules: Market & Technology Trends



Session Contents

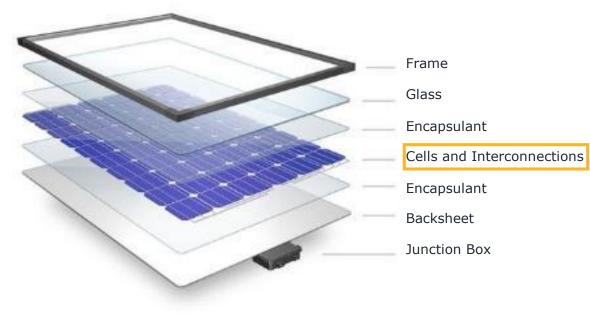
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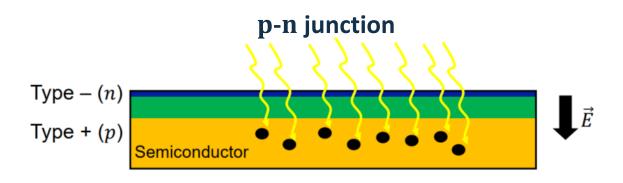


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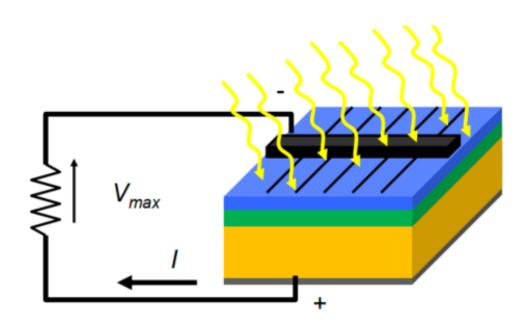
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What is a solar cell?



- Intrinsic (pure) **semiconductor** material (e.g. Si).
- Doped with impurities to become conductor $\rightarrow +(p) \circ -(n)$ charges transporting the current
- Under light \rightarrow absorption of **photons** if $hv > E_g$ (E_g : semiconductor bandgap).
- Photons absorbed in p-zone transfer their energy to electrons (in n-zone to holes).
- Photogenerated carriers move towards the junction and cross it.

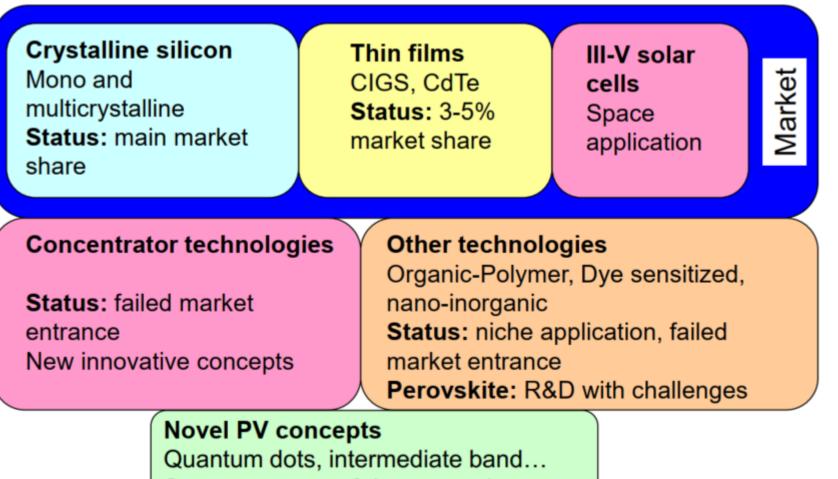


Metallic contacts extract the current

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PV Technology Trends



Status: attempt of demonstration

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Crystalline Silicon (c-Si)



Wafer based (bulk semiconductor)

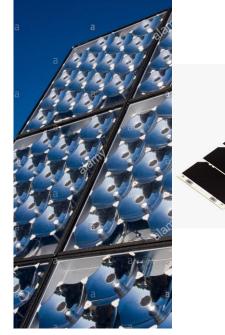
- Processing of wafers
- Series connection of individual solar cells

Thin film





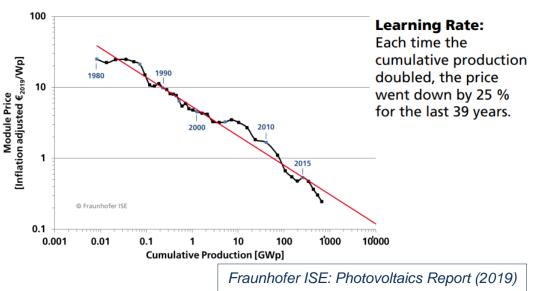
III-V multi-junction



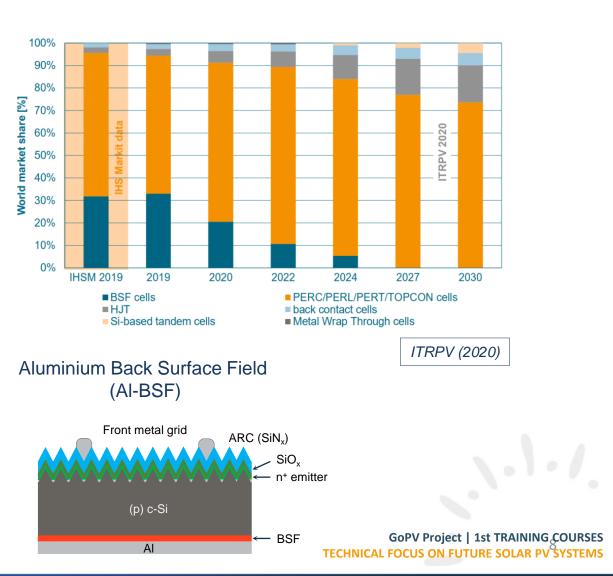


- Deposition on large area substrate
- "Monolithic series integration" of the cells
- Grown epitaxially on crystalline wafers
- Developed for space applications → very costly
- Used in concentrated PV (CPV)





- **1980-1990's** → high performance industrial cells (Si from microelectronics research).
 - Challenges: reduce cost by a factor or 20-30.
- 2000-2010 → strong market development → high demand of PV modules → Si shortage
- 2010 decade → production moving to China → lower production costs, reduction in module prices, lower electricity prices



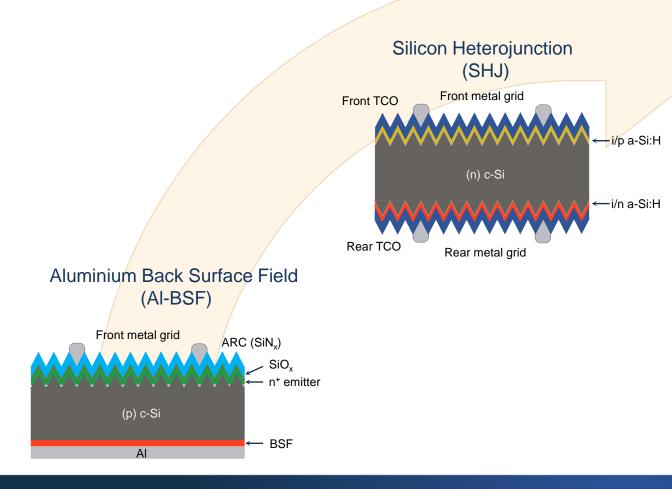
ΈΡΕΙ



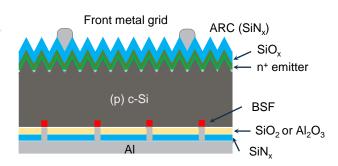


High Temperature Process

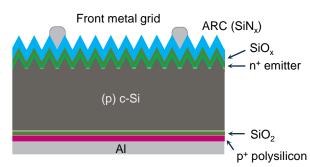
Low Temperature Process



Passivated Emitter and Rear Contact (PERC)





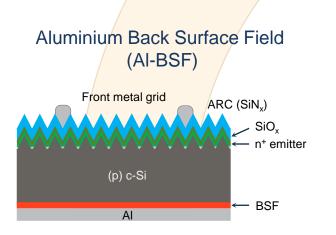


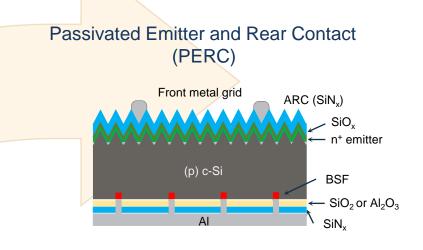




Challenges:

- **Optical losses** → reflection of photons at rear surface)
- Recombination losses → at metallic contact
- Ohmic losses \rightarrow high series resistance at interfaces





- Better passivation (rear surface)
- ↓ Optical losses
- Similar manufacturing
- High temperature processing

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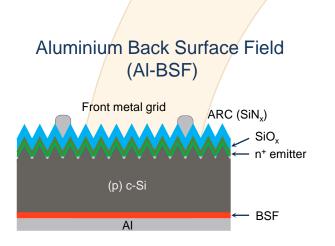


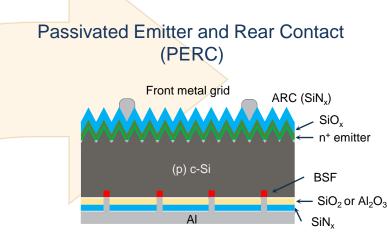


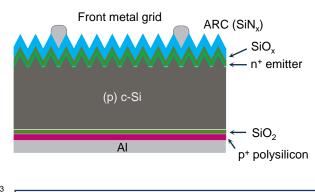
High Temperature Passivating Contact (HTPC)

Challenges:

- **Optical losses** → reflection of photons at rear surface)
- **Recombination losses** → at metallic contact
- Ohmic losses \rightarrow high series resistance at interfaces







- Better passivation
- Simple manufacturing processes → no patterning
- Better passivation (rear surface)
- ↓ Optical losses
- Similar manufacturing
- High temperature processing

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Challenges:

- **Optical losses** \rightarrow reflection of photons at • rear surface)
- **Recombination losses** → at metallic • contact

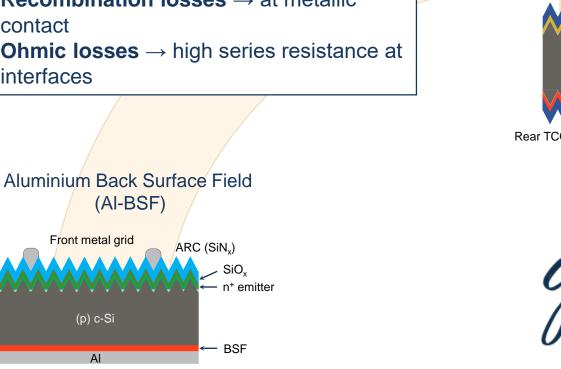
(AI-BSF)

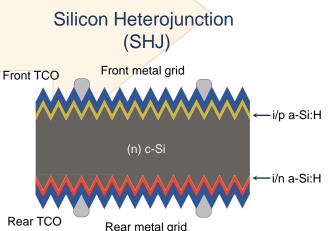
Front metal grid

(p) c-Si

Ohmic losses \rightarrow high series resistance at ٠ interfaces

ARC (SiN_x)







Advantages:

- Better passivation
- ↓ Processing temperature
- ↓ Thickness, ↓ cost ٠
- \uparrow Open-circuit voltage (V_{OC})
- ↓ Temperature coefficient

Challenges:

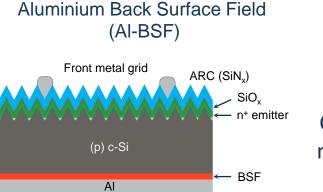
- Need of ECA for soldering
- ↓ adhesion fingers/TCO

ECA: electrically conductive adhesive TCO: transparent conductive oxide



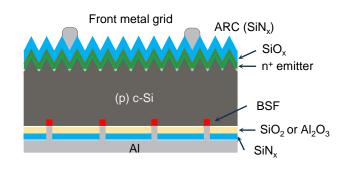


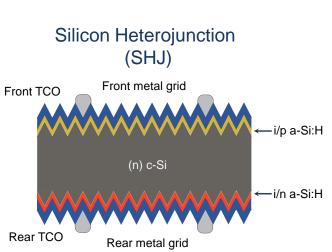
Potential for bifaciality



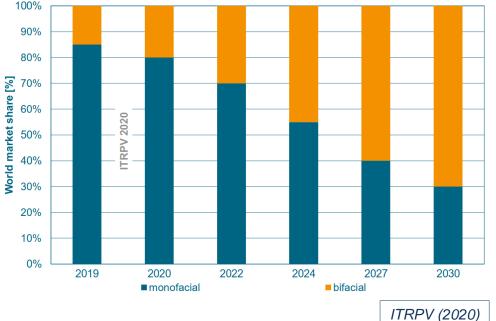
Conventional AI-BSF cells do not give option for bifaciality

Passivated Emitter and Rear Contact (PERC)





Bifacial cell in world market



Novel solar cell concepts promote the development of bifacial technology

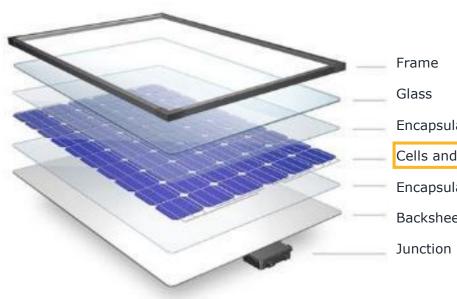
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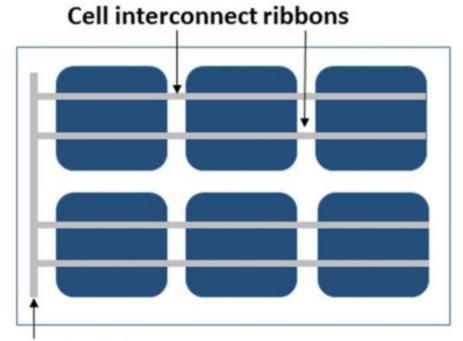
Cell interconnections



Encapsulant

Cells and Interconnections

- Encapsulant
- Backsheet
- Junction Box

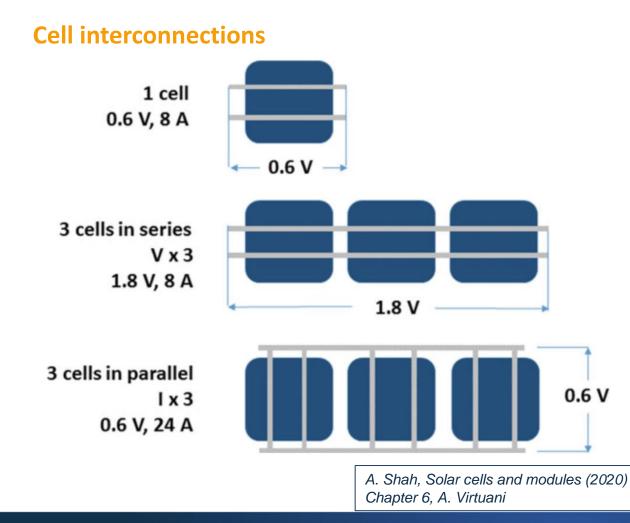


String interconnect

A. Shah, Solar cells and modules (2020) Chapter 6, A. Virtuani







Commercial c-Si modules have 60/72 series-connected solar cells

Series connection \rightarrow to get high voltage

Cells must be current-matched

- **Parallel connection** \rightarrow currents add up
 - Voltages of cells/strings need to be balanced

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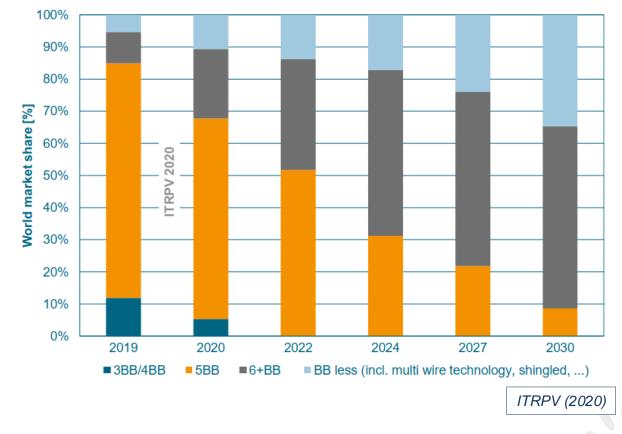


Cell interconnections

Busbar technology



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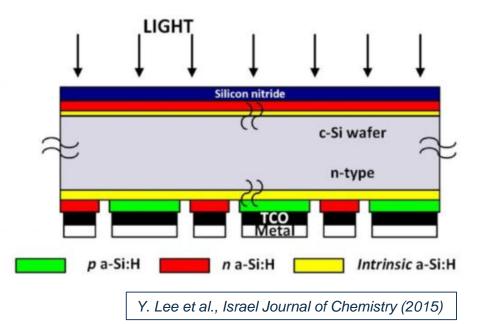
Clear trend for a higher amount of busbars or none at all

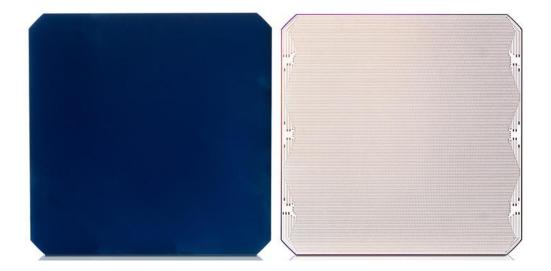




Cell interconnections

Interdigitated Back Contact (IBC) solar cells – no need of busbars





Advantages:

- More aesthetically appealing.
- Ideal candidate for Building Integrated PV (BIPV)

Challenges:

• Cost



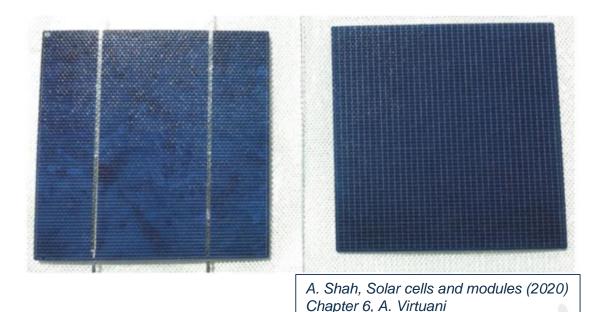


Cell interconnections

Smart-wire technology (SWT)

Multi-ribbon/multi-wire technology (MWT)

- Conventional ribbons and busbars replaced by round wires with small diameter.
- Ribbons (3-6) replaced by 20+ wires.
- \uparrow ribbons $\rightarrow \downarrow$ current distribution \rightarrow wires with lower conductance
- \downarrow silver consumption
- ↓ sensitivity of cell/module to cracks/breakages → ↑ durability







Cell interconnections

Silicon Heterojunction solar cells

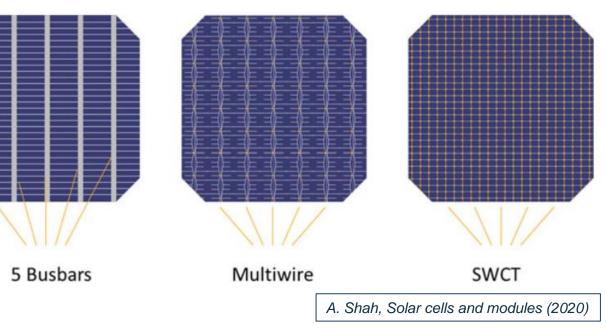
Conventional soldering processes require **high** temperatures \rightarrow need to find a solution for SHJ

Low temperature process → ribbons are "soldered" by using:

- **Busbar technology** → not mainstream
- Electrically Conductive Adhesives (ECA)
- MWT/SWCT.

Challenges:

- Use of more silver.
- More expensive.
- Need to adapt stringers in commercial manufacturing processes.









Why don't we install bare cells in the field?

PV modules are exposed to **external stressors**:

- Temperature variations due to performance and environment.
- High humidity conditions.
- Mechanical stress \rightarrow rain, dust, hail..
- UV radiation

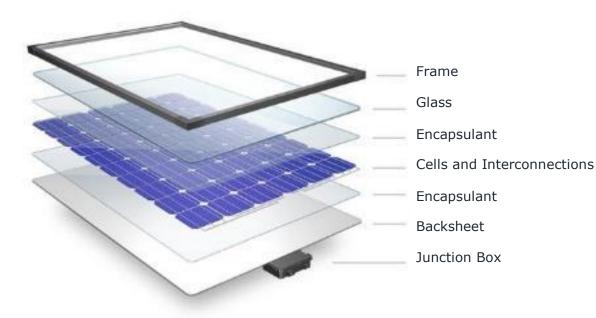
Solar cells and interconnections are **encapsulated/packaged** to:

- 1. Protect electrical circuit from weathering.
- 2. Provide **structural stability** and protect **mechanical integrity.**
- **3.** Isolate electrical circuit from environment (e.g. protect operators from electrical shocks).



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Modules – Innovative concepts



Innovative module concepts target:

- 1. Increased module performance by reducing Cell-to-Module losses.
- 2. Increased energy-yield.
- 3. Increased reliability.





Modules – Innovative concepts

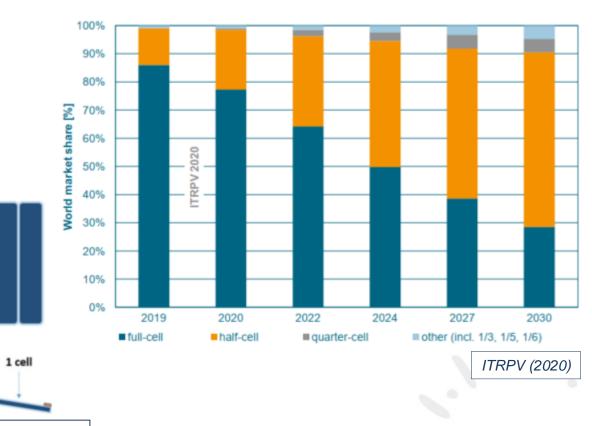
- **1.** Increased module performance
- 2. Increased energy-yield
- 3. Increased reliability

Half-cell modules $\rightarrow \downarrow$ cell interconnection losses

Shingled solar cells $\rightarrow \downarrow$ inactive space

Light capturing ribbons $\rightarrow \downarrow$ shadding losses

Different cell dimensions in c-Si modules



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1 cell

(a) Top view

(b) Side view



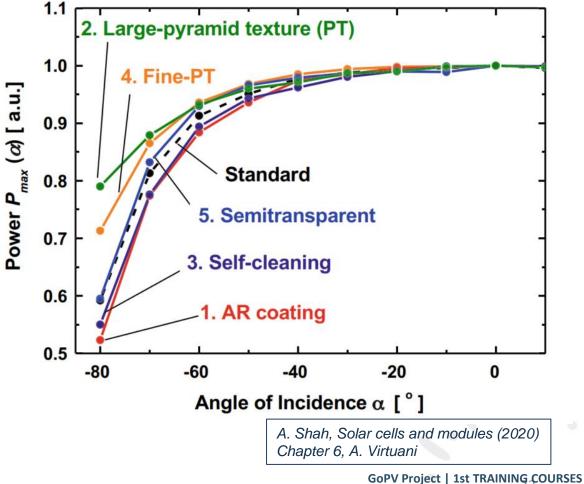


Modules – Innovative concepts

- 1. Increased module performance
- 2. Increased energy-yield
- 3. Increased reliability

Anti-reflection coatings (ARC) on front surface of front glass $\rightarrow \downarrow$ reflection at front glass/air interface

Textured glass \rightarrow \uparrow collection of light at low angles



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Modules – Innovative concepts

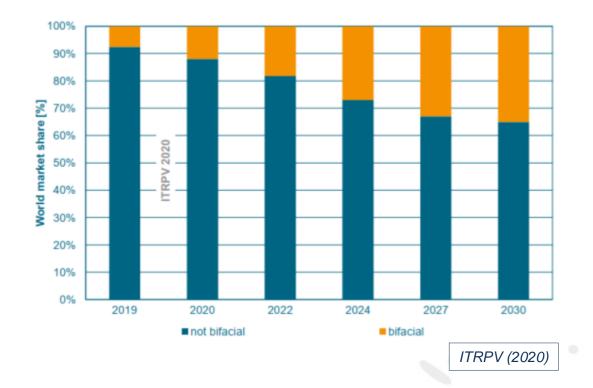
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Anti-reflection coatings (ARC) on front surface of front glass $\rightarrow \downarrow$ reflection at front glass/air interface

Textured glass \rightarrow \uparrow collection of light at low angles

Bifacial cells/modules \rightarrow collection of sunlight reflected by ground

Bifacial Module Technology







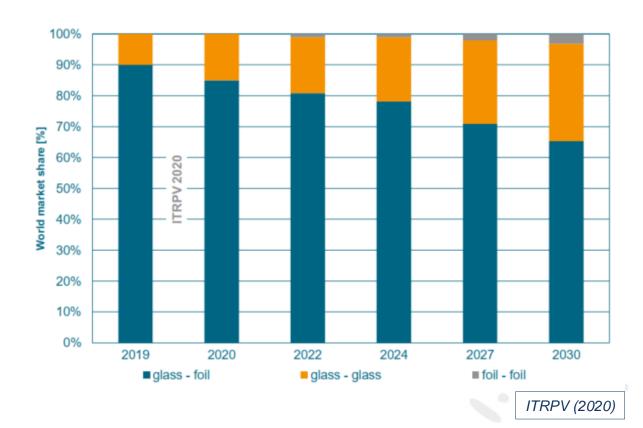
Modules – Innovative concepts

- 1. Increased module performance
- 2. Increased energy-yield
- 3. Increased reliability

Glass/glass modules \rightarrow additional protection in harsh environments (snow, hail or wind loads)

SWT and MWT \rightarrow minimize the impact of cracks on the performance.

Different back cover materials for modules





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